Clock OSC
Product name SG7050CAN 30.000000 MHz TJGA
Product Number／Ordering code X1G0044810031xx
Please refer to the 8．Packing information about xx （last 2 digits）

Output waveform CMOS
Pb free／Complies with EU RoHS directive
Reference weight Typ． 147 mg

| 1．Absolute maximum ratings |  |  |  |  |  |  |
| :--- | :---: | :---: | :---: | :---: | :---: | :--- |
| Parameter | Symbol | Min． | Typ． | Max． | Unit | Conditions／Remarks |
| Maximum supply voltage | Vcc－GND | -0.3 | - | +4 | V | - |
| Storage temperature | T＿stg | -40 | - | +125 | $\circ$ | OC |
| Input voltage | Vin | -0.3 | - | Vcc +0.3 | V | ST terminal |


| 2．Specifications（characteristics） |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Parameter | Symbol | Min． | Typ． | Max． | Unit | Conditions／Remarks |
| Output frequency | f0 |  | 30.000000 |  | MHz |  |
| Supply voltage | Vcc | 1.6 | － | 3.63 | V | － |
| Operating temperature | T＿use | －40 | － | ＋85 | ${ }^{\circ} \mathrm{C}$ | － |
| Frequency tolerance | f＿tol | －50 | － | 50 | $\times 10^{-6}$ | T＿use |
| Current consumption | Icc | － | － | 2.2 | mA | No load condition $\mathrm{Vcc}=3.6 \mathrm{~V}$ |
| Stand－by current | I＿std | － | － | 2.7 | MA | ST＝GND |
| Disable current | I＿dis | － | － | － | mA |  |
| Symmetry | SYM | 45 | － | 55 | \％ | 50\％Vcc Level L＿CMOS＝＜15pF |
| Output voltage | $\mathrm{V}_{\text {OH }}$ | Vcc－0．4 | － | － |  |  |
|  | $\mathrm{V}_{\text {OL }}$ | － | － | 0.4 |  |  |
| Output load condition | L＿CMOS | － | － | 15 | pF | CMOS Load |
| Input voltage | $\mathrm{V}_{\text {IH }}$ | 0.8 Vcc | － | － |  | ST terminal |
|  | $\mathrm{V}_{\text {IL }}$ | － | － | 0.2 Vcc |  | ST terminal |
| Rise time | $\mathrm{t}_{\mathrm{r}}$ | － | － | 4 | ns | Vcc1．6V ：0．2Vcc to 0．8Vcc Level， L＿CMOS＝15pF |
| Fall time | tf | － | － | 4 | ns | Vcc $1.6 \mathrm{~V}: 0.2 \mathrm{Vcc}$ to 0.8 Vcc Level， L＿CMOS＝15pF |
| Start－up time | t＿str | － | － | 3 | ms | $\mathrm{t}=0 \mathrm{at} 0.9 \mathrm{Vcc}$ |
| Jitter | $\mathrm{t}_{\mathrm{DJ}}$ | － | 0 | － | ps | Deterministic J Jiter Vcc＝3．3V |
|  | $\mathrm{t}_{\text {RJ }}$ | － | 2.4 | － | ps | Random Jitter Vcc＝3．3V |
|  | $\mathrm{t}_{\text {tMs }}$ | － | 2.3 | － | ps | $\delta$（RMS of total distribution）Vcc＝3．3V |
|  | $\mathrm{t}_{\text {p－p }}$ | － | 20 | － | ps | Peak to Peak Vcc＝3．3V |
|  | $\mathrm{t}_{\text {acc }}$ | － | 2.5 | － | ps | Accumulated Jitter（ठ）n＝2 to 50000 <br> cycles |
| Phase jitter | $\mathrm{t}_{\mathrm{p}}$ | － | 0.34 | － | ps | Off seet Frequency： 12 kHz to 20MHz，Vcce＝3．3V |
| Phase noise | L（f） | － | － | － | $\mathrm{dBc} / \mathrm{Hz}$ |  |
|  |  | － | －94 | － | $\mathrm{dBc} / \mathrm{Hz}$ | Off set $10 \mathrm{~Hz} \mathrm{Vcc=3.3V}$ |
|  |  | － | －123 | － | $\mathrm{dBc} / \mathrm{Hz}$ | Off set $100 \mathrm{~Hz} \mathrm{VCc}=3.3 \mathrm{~V}$ |
|  |  | － | －145 | － | dBc／Hz | Off set $1 \mathrm{kHz} \mathrm{Vcc}=3.3 \mathrm{~V}$ |
|  |  | － | －155 | － | dBc／Hz | Off set $10 \mathrm{kHz} \mathrm{Vcc=3.3V}$ |
|  |  | － | －158 | － | dBc／Hz | Off set $100 \mathrm{kHz} \mathrm{V}_{\text {cc }}=3.3 \mathrm{~V}$ |
|  |  | － | －159 | － | $\mathrm{dBc} / \mathrm{Hz}$ | Off set $1 \mathrm{MHz} \mathrm{Vcc}=3.3 \mathrm{~V}$ |
| Frequency aging | f＿age | －3 | － | 3 | $\times 10^{-6}$ | $@+25^{\circ} \mathrm{C}$ first year |

## 3．Timing chart



## 4．Test circuit

1）Waveform observation


2）Current consumption

＊Current consumption under the disable function should be ＝GND．

3）Condition
（1）Oscilloscope
－Band width should be minimum 5 times higher（wider）than measurement frequency．
－Probe earth should be placed closely from test point and lead length should be as short as possible
＊Recommendable to use miniature socket．（Don＇t use earth lead．）
（2）L＿CMOS also includes probe capacitance．
（3）By－pass capacitor（ $0.01 \mu \mathrm{~F}$ to $0.1 \mu \mathrm{~F}$ ）is placed closely between VCC and GND．
（4）Use the current meter whose internal impedance value is small．
（5）Power supply
－Start up time（ $0 \%$ VCC to $90 \%$ VCC ）of power source should be more than $150 \mu \mathrm{~s}$ ．
－Impedance of power supply should be as lowest as possible．


## 6．Footprint（Recommended）（Unit：mm）

To maintain stable operation，provide a 0．01uF to 0.1 F by－pass capacitor at a location as near as possible to the power source temminal of the coystal product（between Voc－GND）．


## 7．Reflow profile

Reflow condition（Follow of JEDEC STD－020D．1）


## 8．Packing information

［ 1 ］Product number last 2 digits code（xx）description
The recommended code is＂00＂
X1G0044810031xx

| Code | Condition | Code | Condition |
| :---: | :--- | :---: | :--- |
| 01 | Any Q＇ty vinyl bag（Tape cut） | 13 | $500 \mathrm{pcs} /$ Reel |
| 11 | Any Q＇ty／Reel | 00 | $1000 \mathrm{pcs} /$ Reel |
| 12 | $250 \mathrm{pcs} /$ Reel |  |  |

（1）Tape dimensions
Material of the Carrier Tape：PS
Material of the Top Tape ：PET＋PE
Unit：mm


| Symbol | A | B | C | D | E | F |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: |
| Value | $\varphi 1.5$ | 4 | 8 | 9.25 | 16 | 2.3 |

（2）Reel dimensions
Center material ：PS
Material of the Reel：PS


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